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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
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ROBERT S. CHAU)
Serial No.: 10/748,559)
Filed: December 29, 2003)
For: A CMOS DEVICE WITH)
METAL AND SILICIDE GATE)
ELECTRODES AND A)
METHOD FOR MAKING IT)
Art Unit: unknown
Examiner: unknown
Attorney Docket: P18244

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is being submitted under 37 C.F.R. §1.97(b). Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with a copy of the reference cited on that form. It is respectfully requested that the cited reference be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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Respectfully submitted,

Dated: May 26, 2004



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Substitute for Form 1449/PTO

**FORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(use as many sheets as necessary)

Sheet 1 of 4

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of

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Attorney Docket Number

10/748,559

Application Number	10/748,559
Filing Date	December 29, 2003
First Named Inventor:	Justin K. Brask et al.
Art Unit	Unassigned
Examiner Name	Unassigned
Attorney Docket Number	42P18244

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

Examiner Signature		Date Considered	
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